

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	128	703/19.ccor.	US-PGPUB; USPAT	OR	ON	2006/09/23 14:35
S2	392	703/13.ccor.	US-PGPUB; USPAT	OR	ON	2006/09/23 14:35
S3	632	703/14.ccor.	US-PGPUB; USPAT	OR	ON	2006/09/23 14:36
S4	459	703/22.ccor.	US-PGPUB; USPAT	OR	ON	2006/09/23 14:48
S5	76	703/20.ccor.	US-PGPUB; USPAT	OR	ON	2006/09/23 14:48
S6	13138	simulat\$4 same hardware	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:10
S7	1333	S6 and suspend\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:11
S8	452	S7 and threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:13
S9	105	S8 and restart	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:15
S10	120406	execut\$3 near3 time	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:17
S11	51	S9 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:17
S12	47	S11 and synchron\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:19
S13	37	S12 and accumulat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:20
S14	32	S13 and tag	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:21
S15	26	S14 and @ad<="20040419"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:46
S16	5553	(real adj time) with simulat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:47
S17	432	S16 and suspend\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:47
S18	74	S17 and restart	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:48

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S19	55	S10 and S18	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:48
S20	44	S19 and synchron\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:49
S21	42	S20 and @ad<="20040419"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/23 16:50
S22	47	("5025364" "5493508" "5493672" "5515525" "5546562" "5590049" "5600579" "5600790" "5623418" "5663900" "5664098" "5673418" "5675771" "5678028" "5768567" "5771370" "5787245" "5801958" "5809450" "5812431" "5815715" "5819065" "5838948" "5848236" "5848270" "5857091" "5862361" "5867399" "5867400" "5870308" "5870585" "5870588" "5872958" "5886899" "5909578" "5913052" "5918035" "5943490" "5946472" "5960181" "5960182" "5963724" "6009256" "6202044" "6212489" "6389379" "6389382").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/23 16:59
S23	3	("6584436").URPN.	USPAT	OR	ON	2006/09/23 17:49

		Results
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10.	(((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and synchron!) and threshold) and hardware [All Sources(- All Sciences -)]	32
9.	((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and synchron!) and threshold [All Sources(- All Sciences -)]	61
8.	(pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and synchron! [All Sources(- All Sciences -)]	161
7.	(pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and suspend [All Sources(- All Sciences -)]	5
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3.	((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(suspend)) and hardware) and restart [All Sources(- All Sciences -)]	59
2.	(pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(suspend)) and hardware [All Sources(- All Sciences -)]	378
1.	pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(suspend) [All Sources(- All Sciences -)]	1741



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#2	((simulat*<and>suspend<and>restart)<and>threshold) <and> (pyr >= 1951 <and> pyr <= 2004)	108
#3	((simulat*<and>suspend<and>restart) <and>threshold<and>synchron*) <and> (pyr >= 1951 <and> pyr <= 2004)	75
#4	((simulat*<and>suspend<and>restart) <and>threshold<and>synchron*<and>accumulat*) <and> (pyr >= 1951 <and> pyr <= 2004)	39
#5	((simulat*<and>suspend<and>restart) <and>threshold<and>synchron*<and>accumulat*<and>tag) <and> (pyr >= 1951 <and> pyr <= 2004)	26

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benefits. In this paper we use execution-driven **simulation** of a scalable multiprocessor to evaluate the
As an example, because of the high cost of **suspending** and **restarting** processors, it is very common
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www.cos.ufrj.br/pub/tech_reps/es38996.ps.gz

[IDRA \(IDeal Resource Allocation\): Computing Ideal.. - Fern'andez Carro..](#) (Correct)
Keywords: Parallel Logic Programming **Simulation** Parallelism Concurrency Performance
Fig. 1. And-Parallel Execution Start_goal Fork **Suspend Restart** A B B P P P Q Q Start_goal
And-Parallel Execution Start_goal Fork **Suspend Restart** A B B P P P Q Q Start_goal Finish_goal Fig. 2.
www.clip.dia.fi.upm.es/~clip/papers/idra-europar96.ps.gz

[Modeling Queueing Systems Using Hierarchical Control Flow Graph.. - Sargent \(1997\)](#) (Correct)
Control Flow Graph Models Robert G. Sargent **Simulation** Research Group, 439 Link Hall, Syracuse
It has three input ports: new-jobs"**suspend-operation**"and "**restart-suspended-job**"and
ports: new-jobs"**suspend-operation**"and "**restart-suspended-job**"and one output port:
erc.cat.syr.edu/srg/HCFG-QueueingModels.ps.gz

[An Overview Of Hierarchical Control Flow Graph Models - Fritz, Sargent \(1995\)](#) (Correct)
Graph Models Douglas G. Fritz Robert G. Sargent **Simulation** Research Group Syracuse University 439 Link
"It has three input ports: new-jobs"**suspend-operation**"and "**restart-suspended-job**"and
ports: new-jobs"**suspend-operation**"and "**restart-suspended-job**"and one output port:
erc.cat.syr.edu/srg/WSC95.HCFGModels.ps.gz

[IDRA \(IDeal Resource Allocation\): A Tool for.. - Fernández, ..](#) (Correct)
of sequential or parallel execution and **simulation**, and the algorithms that allow implementing the
Start_goal Start_execution Fork Finish_goal **Suspend Restart** Figure 2: Or-Parallel Execution Node
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I Introduction - ETRI Journal, 2003 - etrij.etri.re.kr

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CO Nwagboso, MA Pendlebury, SK Mukarram - Measurement Science and Technology, 2004 - iop.org

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S Matthews-Frederick - Department of Information Technology and Electrical ..., 2004 - innovexpo.itee.uq.edu.au

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J Picone - 2001 - ece.msstate.edu

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SB House - 1998 - hegel.itc.ku.edu

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V Lohweg, C Diederichs, D MÜLLER - EURASIP Journal on Applied Signal Processing, 2004 - hindawi.com

... Algorithms for **Hardware**-Based Pattern Recognition ... Also Eichhorn [9] and Page 2. Algorithms for **Hardware**-Based Pattern Recognition 1913 ...[Related Articles](#) - [View as HTML](#) - [Web Search](#)[Algorithm Design for Networked Information Technology Systems](#)

S Ghosh - 2003 - books.google.com

... with the socially accepted, strict, legal **threshold** implied in ... problems ranges from distributed discrete- event **simulation** of **hardware** description models in ...[Web Search](#) - [Library Search](#)[The Link-Board Control in the RPC Trigger System for the CMS Experiment](#)

D Ungaro - HIP, 2004 - thesis.helsinki.fi

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T de Doctorat - 2003 - lphe.epfl.ch

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J Li - 2004 - engr.oregonstate.edu

... 7 4.1 **Simulation** results of CMOS inverter ... 4 power-up or standby, it is desirable to run the ... The minimal addition of analog **hardware** for calibration keeps the ...[Cited by 1](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#) - [Library Search](#)[Design and implementation of a multicast, input-buffered ATM switch for the iPOINT testbed - group of 2 »](#)

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K Nahrstedt - 1995 - cis.upenn.edu

... due to resid- ing in a kernel, where they have access to real-time services of the OS, and (3) **hardware** support with better timer ...

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A Goel - 2003 - eecg.toronto.edu

... Dedication To my parents, whose wholehearted support helped me start this endeavor. ...

5.8 Square wave **simulation** . . . normally reserved for dedicated **hardware**. ...

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A Simon - 1993 - osti.gov

... About the Cover: A remote camera **suspended** from the OMEGA ... only when the TPI is well above its **threshold**. ... dimensional **simulation**, an initial single-wavelength ...

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M Diaz, P Owezarski, P S  nac - 1999 - books.google.com

... of the QoS Required by a Distributed Inter -active **Simulation** Application in ... 2. **Hardware** Acceleration PCI Card Page 18. ... 8. Start the FPGAs using control register ...

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" The Artilect War" First Draft Prof. Dr. Hugo de Garis

S Draft - Update, 1999 - cs.usu.edu

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Dirk Grunwald, Soraya Ghiasi

November 2002

Proceedings of the 35th annual ACM/IEEE international symposium on Microarchitecture

Publisher: IEEE Computer Society Press

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2 [Special section: Reasoning about structure, behavior and function](#)



B. Chandrasekaran, Rob Milne

July 1985

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Publisher: ACM Press

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3 [Experience Using Multiprocessor Systems—A Status Report](#)



Anita K. Jones, Peter Schwarz

June 1980

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Publisher: ACM Press

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4 [Characterizing the caching and synchronization performance of a multiprocessor operating system](#)



Josep Torrellas, Anoop Gupta, John Hennessy

September 1992

ACM SIGPLAN Notices, **Proceedings of the fifth international conference on Architectural support for programming languages and operating systems ASPLOS-V**, Volume 27 Issue 9

Publisher: ACM Press

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5 [Logged virtual memory](#)



D. R. Cheriton, K. J. Duda

December 1995

ACM SIGOPS Operating Systems Review, **Proceedings of the fifteenth ACM symposium on Operating systems principles SOSP '95**, Volume 29 Issue 5

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Vincenzo Piuri

June 1990

Proceedings of the 3rd international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 2 IEA/AIE '90

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Paul Suetens, Pascal Fua, Andrew J. Hanson

March 1992

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8 [Survey of software tools for evaluating reliability, availability, and serviceability](#)



Allen M. Johnson, Miroslaw Malek
September 1988 **ACM Computing Surveys (CSUR)**, Volume 20 Issue 4

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The J-machine multicomputer: an architectural evaluation

Michael D. Noakes, Deborah A. Wallach, William J. Dally

May 1993 **ACM SIGARCH Computer Architecture News , Proceedings of the 20th annual international symposium on Computer architecture ISCA '93**, Volume 21 Issue 2

Publisher: ACM Press

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AdJava: automatic distribution of Java applications

Mohammad M. Fuad, Michael J. Oudshoorn

January 2002 **Australian Computer Science Communications , Proceedings of the twenty-fifth Australasian conference on Computer science - Volume 4 ACSC '02**, Volume 24 Issue 1

Publisher: Australian Computer Society, Inc., IEEE Computer Society Press

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VC-1: a scalable graphics computer with virtual local frame buffers

Satoshi Nishimura, Tosiya L. Kunii

August 1996 **Proceedings of the 23rd annual conference on Computer graphics and interactive techniques**

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Mostly lock-free malloc

Dave Dice, Alex Garthwaite

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Tuning high-performance scientific codes: the use of performance models to control resource usage during data migration and I/O

Jonghyun Lee, Marianne Winslett, Xiaosong Ma, Shengke Yu

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Run-time support for distributed sharing in safe languages

Y. Charlie Hu, Weimin Yu, Alan Cox, Dan Wallach, Willy Zwaenepoel

February 2003 **ACM Transactions on Computer Systems (TOCS)**, Volume 21 Issue 1

Publisher: ACM Press

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A distributed, operating system based, blackboard architecture for real-time control

Daniel L. Lerner

June 1990 **Proceedings of the 3rd international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1 IEA/AIE '90**

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